



Docket Number: 040008-0307459
Client Reference: OG03-028

PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re the Application of

Chee Hong CHOI, et al.

Group Art Unit: 2812

Application No.: 10/747,618

Examiner: Unassigned

Filed: December 30, 2003

Confirmation No.: 2872

For: METHOD FOR FABRICATING TRANSISTOR OF SEMICONDUCTOR DEVICE

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450

Sir:

Pursuant to 37 CFR 1.56, the attention of the Patent and Trademark Office is hereby directed to the reference(s) listed on the attached PTO-1449. Unless otherwise indicated herein, one copy of each reference is attached. It is respectfully requested that the information be expressly considered during the prosecution of this application, and that the reference(s) be made of record therein and appear among the "References Cited" on any patent to issue therefrom. Applicants respectfully request the Examiner return an initialed copy of the enclosed Form PTO-1449 to Applicants with the next Office communication to indicate that the reference(s) has been considered, per MPEP § 609.

This Information Disclosure Statement is being filed (a) within three months of the U.S. filing date of this non-CPA application, OR (b) before the mailing date of the first Office Action on the merits in the present application. No certification or fee is required. English-language Abstracts of the non-English language references are attached hereto.

Respectfully Submitted,

Glenn T. Barrett
Registration Number 38705
Customer Number: 00909

Date: April 29, 2004
PILLSBURY WINTHROP LLP
Telephone: (703) 905-2000
Facsimile: (703) 905-2500
P.O. Box 10500
McLean, VA 22102

□□

FORM PTO-1449 (modified)
 To: U.S. Department of Commerce
 (PW FORM PAT-1449)
 Patent and Trademark Office

Atty.
 Dkt. No.

M#

Client Ref.

307459

OG03-028

Applicant: CHOI et al.

Appln. No.: 10/747,618

Filing Date: December 30, 2003

Examiner: Unassigned Group Art Unit: 2812

Date: April 29, 2004

Page **1** of **1**

U.S. PATENT DOCUMENTS

Examiner's Initials*	Document Number	Date MM/YYYY	Name (Family Name of First Inventor)	Class	Sub Class	Filing Date (if appropriate)
AR	6238988	05/2001	Hsiao et al.			
BR	5864163	01/1999	Chou et al.			
CR						
DR						
ER						
FR						
GR						
HR						
IR						
JR						
KR						
LR						
MR						
NR						

FOREIGN PATENT DOCUMENTS

	Document Number	Date MM/YYYY	Country	Inventor Name	English Abstract		Translation Readily Available	
					Enclosed	No	Enclose	No
OR	JP2002-184876	06/2002	Japan	Fujio		x		
PR								
QR								
RR								
SR								
TR								
UR								
VR								
WR								
XR								

OTHER (Including in this order Author, Title, Periodical Name, Date, Pertinent Pages, etc.)

YR			
ZR			
AAR			
BBR			
CCR			
DDR			

Examiner

Date Considered:

*EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP § 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.